

Protocol Verification and State Space Methods

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Outline

- 1 **Lecture 2: Introduction to model checking**
- 2 **Lecture 3a: Specification and model checking of Time Petri Nets and Timed Automata**
- 3 **Lecture 3b: CPN, modules, and data types**
- 4 **Lecture 4a: Parametric model checking for PN**
- 5 **Lecture 4b: Model checking CPN**
- 6 **Lecture 5a: Case studies using VerICS**
- 7 **Lecture 5a: Case studies using CPN Tools**

Introduction to model checking

Outline

- Standard non-symbolic model checking algorithms for CTL and LTL.
- Partial order reductions for LTL_{-X} and CTL_{-X} .
- Introduction to symbolic model checking for CTL.
- BDD- and SAT-based model checking.

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Introduction to model checking

Standard non-symbolic model checking algorithms for CTL and LTL.

Model checking for Kripke models

Model checking problem

M

a Kripke model

?

\models

φ

a modal formula

Syntax

- S1.** every member of \mathcal{PV} is a state formula,
 - S2.** if φ and ψ are state formulas, then so are $\neg\varphi$ and $\varphi \wedge \psi$,
 - S3.** if φ is a path formula, then $A\varphi$ and $E\varphi$, are state formulas,
 - P1.** any state formula φ is also a path formula,
 - P2.** if φ, ψ are path formulas, then so are $\varphi \wedge \psi$ and $\neg\varphi$,
 - P3.** if φ, ψ are path formulas, then so is $X\varphi$, $G\varphi$, and $\varphi U\psi$.
- CTL*** consists of the set of all state formulae.

Variety of sublogics of CTL*

Definition

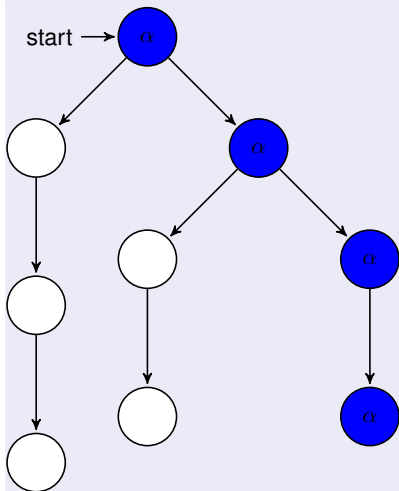
- **LTL** \subset CTL* is the fragment of CTL* in which all modal formulas are of the form $A\varphi$, where φ does not contain the state modalities A, E.
- **CTL** \subset CTL* is the fragment of CTL* in which A, E, and the path modalities U and G may only appear paired: AX, EX, AU, EU, AG, and EG.

Semantics of CTL*

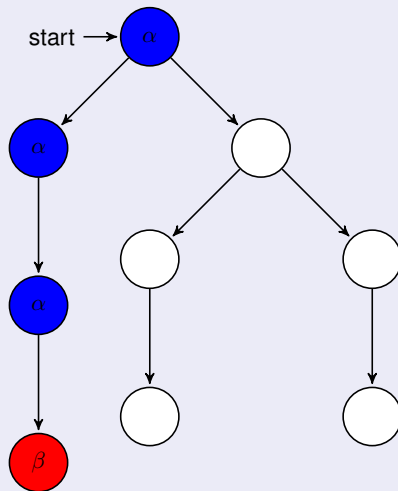
$M = (G, \iota, \Pi, V)$ - a model and $\pi = g_0 a_0 g_1 \dots$ - an infinite path of G .
 π_i denotes the suffix $g_i a_i g_{i+1} \dots$ of π

- S1.** $g \models q$ iff $q \in V(g)$, for $q \in PV$,
- S2.** $g \models \neg\varphi$ iff not $g \models \varphi$,
 $g \models \varphi \wedge \psi$ iff $g \models \varphi$ and $g \models \psi$,
- S3.** $g \models A\varphi$ iff $\pi \models \varphi$ for every path π starting at g ,
 $g \models E\varphi$ iff $\pi \models \varphi$ for some path π starting at g ,
- P1.** $\pi \models \varphi$ iff $g_0 \models \varphi$ for any state formula φ ,
- P2.** $\pi \models \neg\varphi$ iff not $\pi \models \varphi$,
 $\pi \models \varphi \wedge \psi$ iff $\pi \models \varphi$ and $\pi \models \psi$,
- P3.** $\pi \models X\varphi$ iff $\pi_1 \models \varphi$,
 $\pi \models G\varphi$ iff $\pi_j \models \varphi$ for all $j \geq 0$,
 $\pi \models \varphi U \psi$ iff there is an $i \geq 0$ such that $\pi_i \models \psi$ and $\pi_j \models \varphi$ for all $0 \leq j < i$.

Semantics in Examples

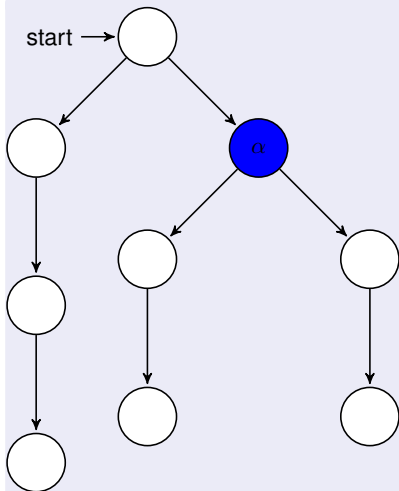


$M, start \models EG\alpha$

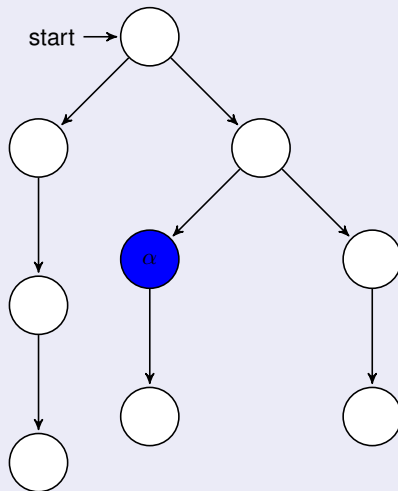


$M, start \models E\alpha U\beta$

Semantics in Examples



$M, start \models EX_{\alpha}$



$M, start \models EF_{\alpha}$

Model checking CTL by state labeling

State labelling

If we do not bother about the size of a model, then the simplest approach to CTL model checking, called **state labelling**, can be used.

Algorithm

We show a deterministic algorithm, based on state labelling, for determining whether a CTL formula φ is true at a state $s \in S$ in a finite model $M = ((S, s^0, \rightarrow), V)$, of time complexity $O(|\varphi| \times (|S| + |\rightarrow|))$.

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Model checking CTL by state labeling

Algorithm

The algorithm is designed so that when it finishes, each state s of M is labelled with the subformulas of φ which are true at s .

- The algorithm operates in stages.
- The i -th stage handles all subformulas of φ of length i for $i \leq |\varphi|$.
- Thus, at the end of the last stage each state will be labelled with all subformulas of φ which are true at it.

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Model checking CTL by state labeling

CTL operators

Each of the operators of CTL can be expressed in terms of the three operators EX, EG, and EU.

Five cases

So, only 5 cases have to be considered, where φ is:

$\neg\psi$,

$\psi_1 \wedge \psi_2$,

EX ψ ,

E(ψ_1 U ψ_2), or

EG ψ .

Algorithm

The algorithm is discussed for the last two cases only, as the others are straightforward.

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Model checking CTL by state labeling

Formula $\varphi = E(\psi_1 U \psi_2)$

- The algorithm first finds all the states which are labelled with ψ_2 and labels them with φ .
- It goes backwards using the relation \rightarrow^{-1} and finds all the states which can be reached by a path in which each state is labelled with ψ_1 .
All such states are labelled with φ .

Complexity

This step requires time $O(|S| + |\rightarrow|)$.

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Formula $\varphi = \text{EG}\psi$

- The graph (S', \rightarrow') is constructed, where $S' = \{s \in S \mid M, s \models \psi\}$ and $\rightarrow' = \rightarrow \cap (S' \times S')$.
- (S', \rightarrow') is partitioned into strongly connected components and those states which belong to the components of size greater than 1 or with a self-loop are selected and labelled with φ .
- The algorithm goes backwards from these states using \rightarrow^{-1} and finds all those states which can be reached by a path in which each state is labelled with ψ . It labels these states with φ .

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Model checking CTL by state labeling

Example

Consider the model **M** shown below and the CTL formula

$$\varphi = E(p_1 U (EG p_2)).$$

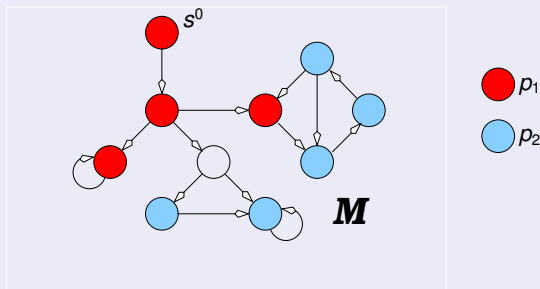
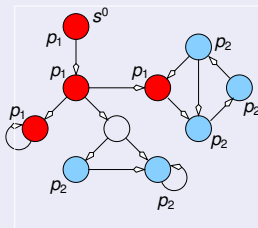


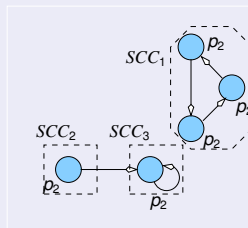
Figure: The model **M**.

Model checking CTL by state labeling

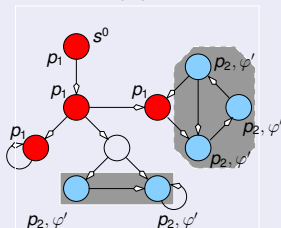
Labelling M with $\varphi = E(p_1 \cup (EGp_2))$; $\varphi' = EGp_2$



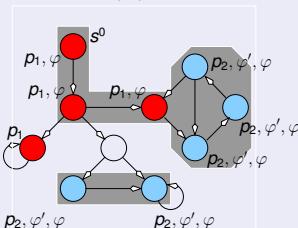
(a)



(b)



(c)



(d)

References and other approaches

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The original state labelling algorithm for CTL was introduced by Clarke, Emerson, and Sistla in 1986.

Automata theoretic approaches

- By checking non-emptiness of the product of the automaton representing a system and an automaton accepting all the models of the negation of a formula, via ...
- A translation from CTL to alternating tree automata.
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Introduction to model checking for knowledge and time

Partial order reductions for LTL_{-X} and CTL_{-X} .

Networks of automata

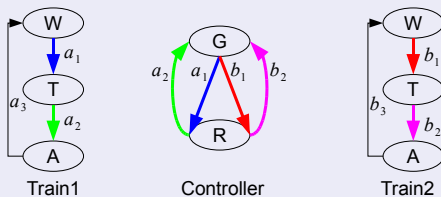


Figure: TC composed of two trains and the controller

Algorithm DFS-POR

DFS-POR

DFS-POR is used to compute paths of the reduced model.

A stack represents the path $\pi = g_0 a_0 g_1 a_1 \cdots g_n$ currently being visited. For g_n , the following three operations are computed in a loop:

- 1 The set $en(g_n) \subseteq Act$ of enabled actions is identified and a subset $E(g_n) \subseteq en(g_n)$ of possible actions is heuristically selected.
- 2 For any action $a \in E(g_n)$ compute the successor state g' of g_n such that $g_n \xrightarrow{a} g'$, and add g' to the stack.
Recursively proceed to explore the submodel originating at g' .
- 3 Remove g_n from the stack.

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Conditions for selection of $E(g)$

Conditions

- C1** No action $a \in Act \setminus E(g)$ that is **dependent** on an action in $E(g)$ can be executed before an action in $E(g)$ is executed.
- C2** On every cycle in the constructed state graph there is at least **one node** g for which $E(g) = en(g)$.
- C3** Each action in $E(g)$ is **invisible**, i.e., does not change $V(g)$.
- C4** If $E(g) \neq en(g)$, then $E(g)$ is a **singleton**.

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Theorem

Let M be a model and $M' \subseteq M$ be the reduced model generated by the *DFS-POR* algorithm. The following conditions hold:

- a) If the choice of $E(g)$ is given by **C1**, **C2**, **C3**, then
 $M \models \varphi$ iff $M' \models \varphi$, for any LTL_{-X} formula φ .
- b) If the choice of $E(g)$ is given by **C1**, **C2**, **C3** and **C4**, then
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Experimental Results - Trains and controller (TC)

TC

Property: if the train 1 is in the tunnel, then no other train is in the tunnel at the same time:

$$AG(\text{in_tunnel}_1 \rightarrow \bigwedge_{i=2}^n \neg \text{in_tunnel}_i),$$

State spaces

$F(n)$ - the size of the full state space.

$R(n)$ - the size of the reduced state space.

- $F(n) = c_n \times 2^{n+1}$, for some $c_n > 1$,
- $R(n) = 3 + 4(n - 1)$.

The reduced state space is *exponentially smaller* than the original one, for both LTL_{-X} and CTL^*_{-X} .

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Introduction to model checking

Introduction to symbolic model checking for CTL.

Introduction

Symbolic and non-symbolic model checking methods can exploit the fixed point characterization of CTL formulas.

Using fixpoints

Labelling the states with the subformulas or computation of OBDD representation of a formula uses the standard algorithms for computing the least and the greatest fixpoints as follows.

Fixed-point verification for CTL

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Fixed points approach

Axioms for CTL

- $\text{EG}\varphi \equiv \varphi \wedge \text{EXEG}\varphi$,
- $\text{E}(\varphi\text{U}\psi) \equiv \psi \vee (\varphi \wedge \text{EX}(\text{E}(\varphi\text{U}\psi)))$,

Fixed point characterization of CTL

Let $\llbracket \varphi \rrbracket = \{s \in S \mid s \models \varphi\}$.

- $\llbracket \text{EG}\varphi \rrbracket = \llbracket \varphi \rrbracket \cap \llbracket \text{EXEG}\varphi \rrbracket$,
- $\llbracket \text{E}(\varphi\text{U}\psi) \rrbracket = \llbracket \psi \rrbracket \cup (\llbracket \varphi \rrbracket \cap \llbracket \text{EXE}(\varphi\text{U}\psi) \rrbracket)$

Pre-set

Let $\text{pre}(X) = \{s \in S \mid (\exists s' \in X) s \rightarrow s'\}$, for $X \subseteq S$.

Characterization

- $\llbracket \text{EG}\varphi \rrbracket = \llbracket \varphi \rrbracket \cap \text{pre}(\llbracket \text{EG}\varphi \rrbracket)$,
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Fixed points approach

Axioms for CTL

- $\text{EG}\varphi \equiv \varphi \wedge \text{EXEG}\varphi$,
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Model checking algorithm based of fixpoint characterization

```
mchk( $M, \varphi$ ) {  
  if  $\varphi \in PV$ , then return  $V^{-1}(\varphi)$ ,  
  if  $\varphi = \neg\psi$ , then return  $S \setminus mchk(M, \psi)$ ,  
  if  $\varphi = \varphi_1 \vee \varphi_2$ , then return  $mchk(M, \varphi_1) \cup mchk(M, \varphi_2)$ ,  
  if  $\varphi = EX\psi$ , then return  $mchk_{EX}(M, \psi)$ ,  
  if  $\varphi = EG\psi$ , then return  $mchk_{EG}(M, \psi)$ ,  
  if  $\varphi = E(\psi_1 U \psi_2)$ , then return  $mchk_{EU}(M, \psi_1, \psi_2)$ ,  
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Algorithm: Model checking procedures

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mchkEX(M,  $\psi$ ){  
  X := mchk(M,  $\psi$ );  
  Y := pre(X);  
  return Y };
```

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mchkEG(M,  $\psi$ ){  
  X := mchk(M,  $\psi$ );  
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  while (Z  $\neq$  Y){  
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OBDD

OBDD (Ordered Binary Decision Diagrams) are used for succinct representation of Boolean functions.

Consider a Boolean function:

$$f : \{0, 1\}^n \longrightarrow \{0, 1\}$$

A function can be represented by the results of all the valuations of some propositional formula over n propositional variables.

Example

For example the function $f(x_1, x_2) = x_1 * x_2$ is represented by the formula $p_1 \wedge p_2$.

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Introduction to OBDDs

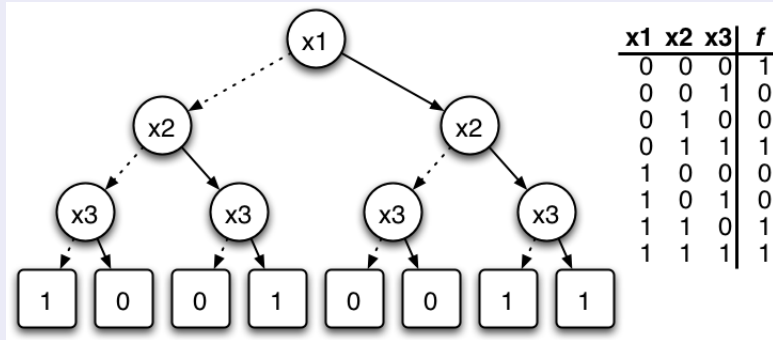


Figure: BDD representing the boolean function f (source: Wikipedia)

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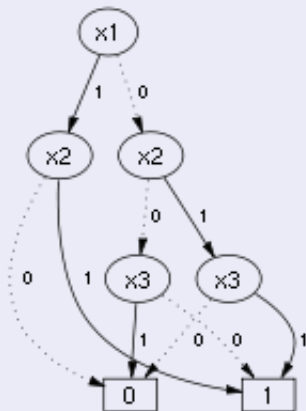


Figure: Canonical OBDD representing the boolean function f (source: Wikipedia)

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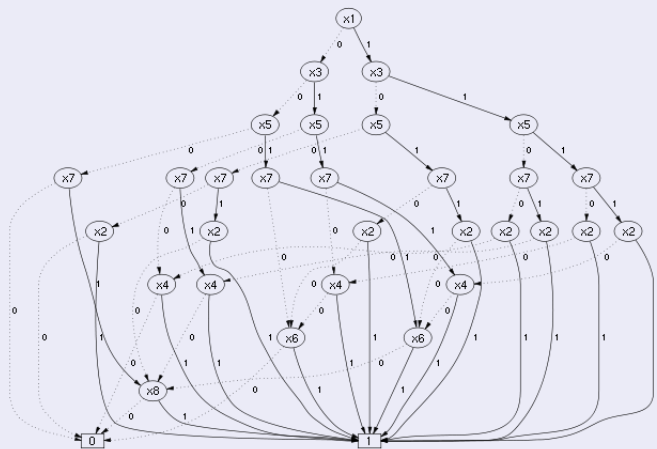


Figure: OBDD with a bad variable ordering (source: Wikipedia)

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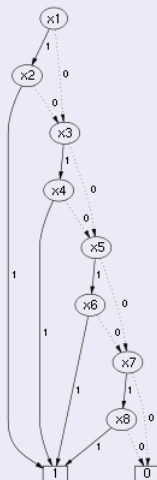


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Operations on OBDDs

The following operations can be implemented by polynomial-time graph manipulation algorithms:

- disjunction,
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OBDD-based model checking for CTLK

Fixed point algorithms on OBDD

The algorithms computing for each formula φ the set of states $\llbracket \varphi \rrbracket$ in which φ holds, can operate on the OBDD representations of the states.

Encoding

This requires to encode the states and the transition relation of a model M by propositional formulas, and then to represent these formulas by OBDDs.

Model checking

$M, s^0 \models \varphi$ is translated to checking whether $s^0 \in \llbracket \varphi \rrbracket$.

$$OBDD(\{s^0\}) \wedge OBDD(\llbracket \varphi \rrbracket) \stackrel{?}{=} OBDD(\emptyset)$$

$OBDD(S)$ denotes the OBDD representing the set of states S .

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- **Problem:** is a propositional formula satisfiable?
- **Theoretical complexity:** NP-complete (Cook, 1971),
- **Practical and efficient SAT solvers:** only in the last decade,
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- **A general idea:** search efficiently for a satisfying assignment.

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- Efficient **data** representation,
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- **Heuristics**: frequently efficient in practice,
- **CNF**: conjunctive normal form, conjunction of disjunctions of literals,

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Two fragments of CTL

Syntax of ACTL

The logic ACTL is the restriction of CTL such that it consists of the formulas of the form: $AX\alpha$, $A(\alpha U \beta)$, $AG\alpha$.

So, the formulas are only in the universal form (no negation applied to modalities).

Syntax of ECTL

The language of ECTL is defined as $\{\neg\varphi \mid \varphi \in \text{ACTL}\}$

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Idea of Bounded Model Checking **BMC**

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- **BMC**: to prove that an ECTL formula holds or that an ACTL formula does not hold in M
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 - * If $\psi \in \text{ACTL}$, take negation $\varphi := \neg\psi$
 - * If $\psi \in \text{ECTL}$, $\varphi := \psi$
 - 2 Take a fragment M' of the model M (preserving φ , i.e., $M' \models \varphi$ implies $M \models \varphi$),
 - 3 Translate $M' \models \varphi$ to a Boolean formula $[M'] \wedge [\varphi]_{M'}$, ($M' \models \varphi$ iff $[M'] \wedge [\varphi]_{M'}$ is satisfiable),
 - 4 Check the satisfiability of $[M'] \wedge [\varphi]_{M'}$ with SAT-solvers.

Conclusion:

If $[M'] \wedge [\varphi]_{M'}$ is satisfiable, then $M \models \varphi$.

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- Let φ be an ECTL formula,
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- Select the k -model M_k (of the paths of length k),
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Define the function $f_k : \text{ECTL} \rightarrow \mathbb{N}$ as follows:

- $f_k(p) = f_k(\neg p) = 0$, where $p \in \mathcal{PV}$,
- $f_k(\alpha \vee \beta) = \max\{f_k(\alpha), f_k(\beta)\}$,
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The function $f_k(\alpha)$ computes the number of symbolic paths (sufficient) to represent submodels of M_k in the propositional translation of α .

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Bounded semantics for ECTLpK

Let $M = (K, \mathcal{V})$ be a model and $k \in \mathbb{N}_+$.

The k -model for M is a structure

$$M_k = ((G, P_k, \iota), \mathcal{V}),$$

where

- G - a set of the global states,
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$$\begin{aligned} s \models \text{EX}\alpha & \quad \text{iff} \quad \exists \pi \in P_k (\pi(0) = s \text{ and } \pi(1) \models \alpha), \\ s \models \text{EG}\alpha & \quad \text{iff} \quad \exists \pi \in P_k (\pi(0) = s \text{ and} \\ & \quad \forall_{0 \leq j \leq k} \pi(j) \models \alpha \wedge \text{loop}(\pi) \neq \emptyset), \\ s \models \text{E}(\alpha \text{U} \beta) & \quad \text{iff} \quad (\exists \pi \in P_k) (\pi(0) = s \text{ and} \\ & \quad \exists_{0 \leq j \leq k} (\pi(j) \models \beta \text{ and } \forall_{0 \leq i < j} \pi(i) \models \alpha)). \end{aligned}$$

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The bounded semantics for $s \models \text{EG}\alpha$ says that there is a k -path π , which starts at s , all its states satisfy α and π is a loop, which means that one of the **states of π** is a \rightarrow -successor of $\pi(k)$.

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Main references and next lecture

References



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